In the Specification:

Please amend page 37, line 17- page 38, line 3 of the application as filed as follows:

In the signal cancellation device according to the present invention, ~~although not~~ as shown in a ~~figure~~ FIG. 19, the first and/or second amplitude adjuster 22 (23) are/is provided with an in-phase splitter, which splits input subsignals B0 (B90) into third and fourth subsignals B0, B0' (B90, B90) of the same phase; a delay device, which delays the fourth subsignal B0' (B90'), an amplitude adjuster, which is able to adjust the amplitude of the third subsignal B0 (B90) in a single phase, and an anti-phase combiner, which combines in opposite phases the third subsignal B0 (B90) after amplitude adjustment and the fourth subsignal B0' (B90') after delay.

Please page 38, line 18- page 39, line 5 of the application as filed as follows:

In the signal cancellation device according to the present invention, ~~although not~~ as shown in a ~~figure~~ FIG. 20, the first and/or second amplitude adjusters 22 (23) are/is provided with a splitter, which splits the input split signals into first, second, and third subsignals B0, B0', and B0" having the same phase; a delay device, which delays the third subsignal B0"; third and fourth amplitude adjusters, which are able to adjust the amplitudes of the first and second subsignals B0, B0' each in a single phase; and a combiner, which combines the first and second subsignals B0, B0' in the same phase after amplitude adjustment and said third subsignal B0" after delay in a freely selected phase in the opposite quadrant as said first and second subsignals B0, B0'.

Please amend the "BRIEF DESCRIPTION OF THE DRAWINGS" section of the application-as-filed to add the following:

FIG. 19 is a block diagram of the amplitude adjuster of the present invention.

FIG. 20 is a block diagram of another implementation of the amplitude adjuster of the present invention.